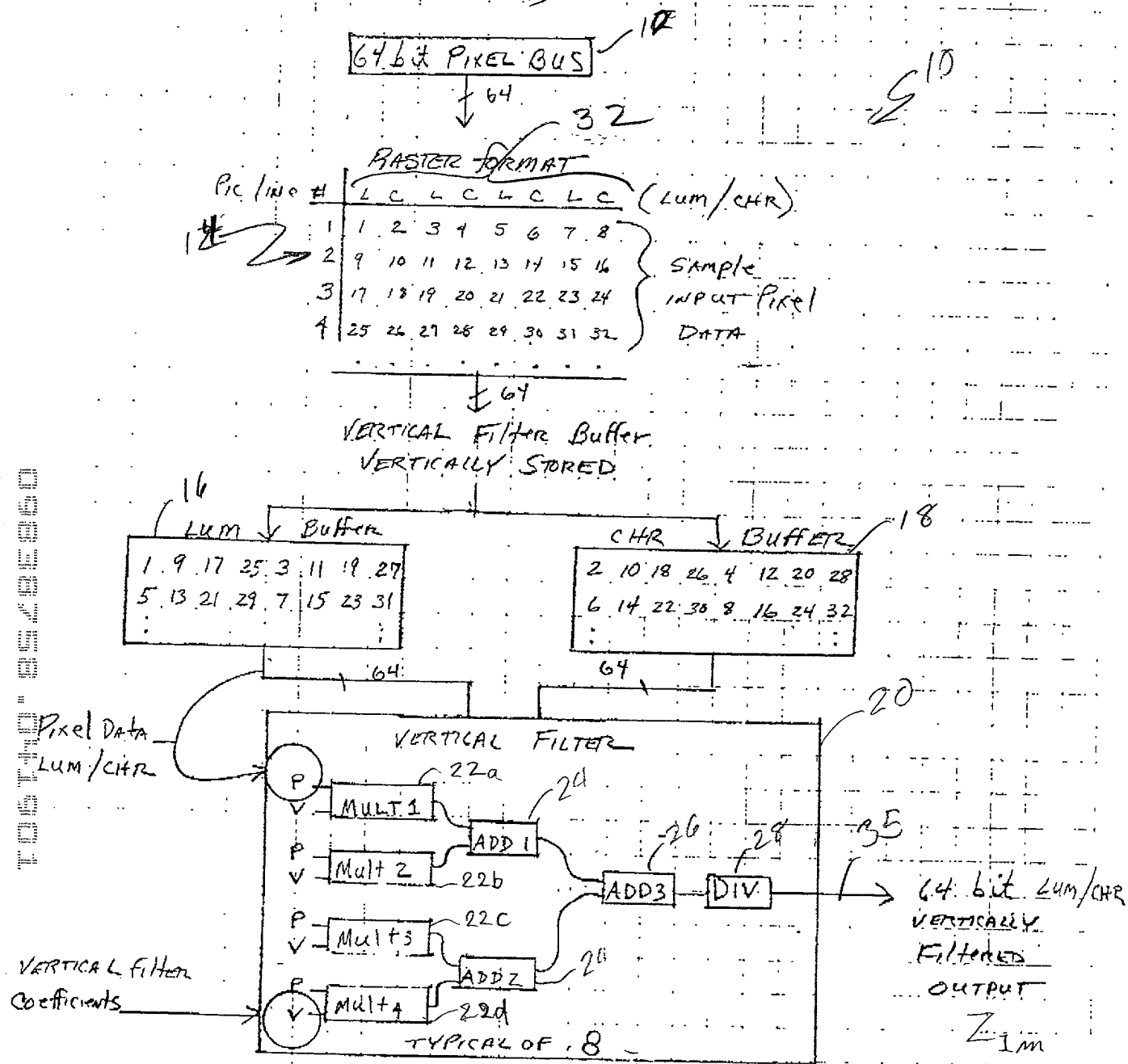


FIGURE 1. SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL SPATIAL FILTERING CIRCUIT.
(RELATED ART)



$$Z_{im} = \frac{P_{rc} V_i + P_{rc} V_i + P_{rc} V_i + P_{rc} V_i}{N}$$

FIGURE 2 SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL CHROMA CONVERSION CIRCUIT.
(RELATED ART)

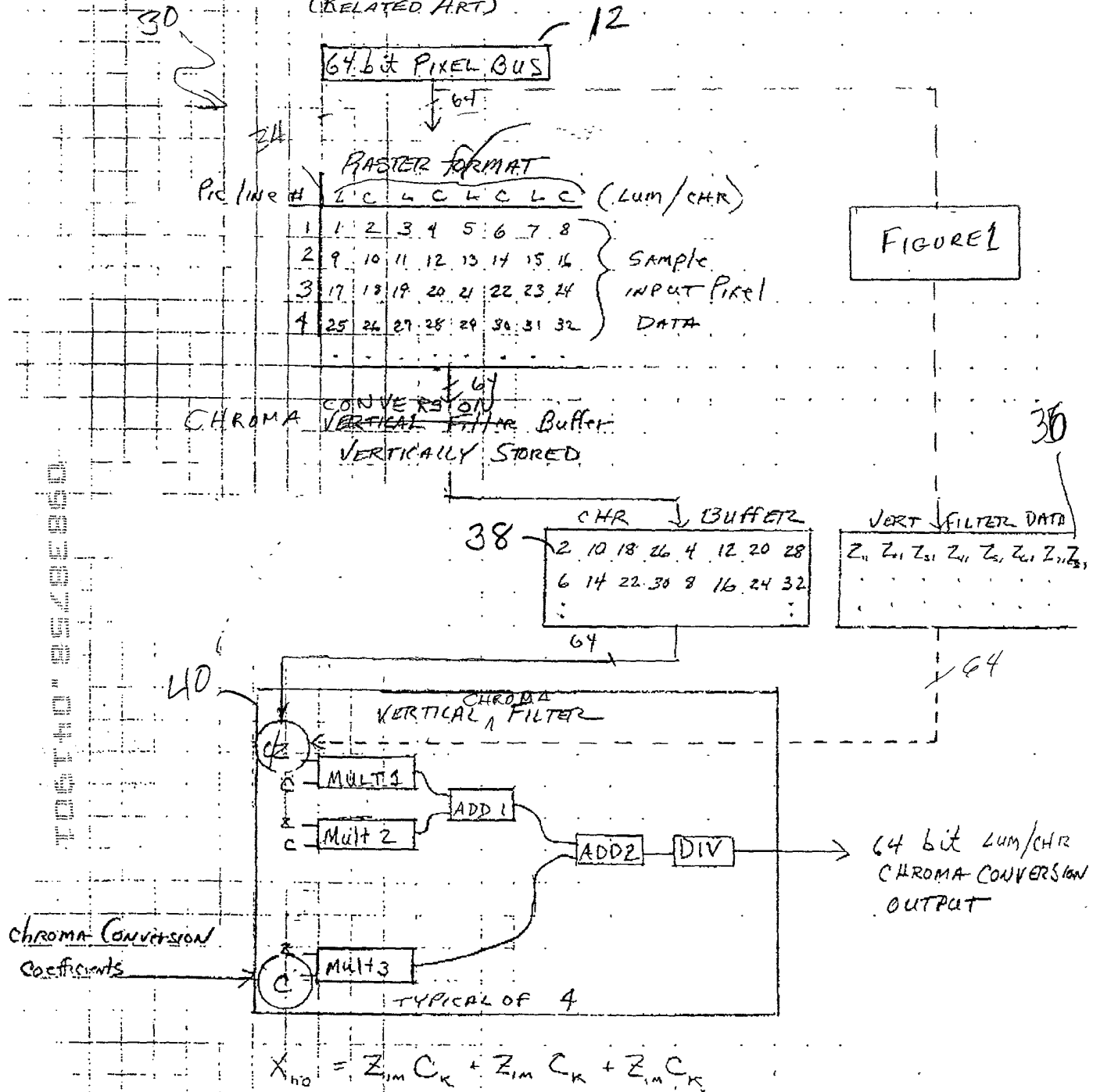
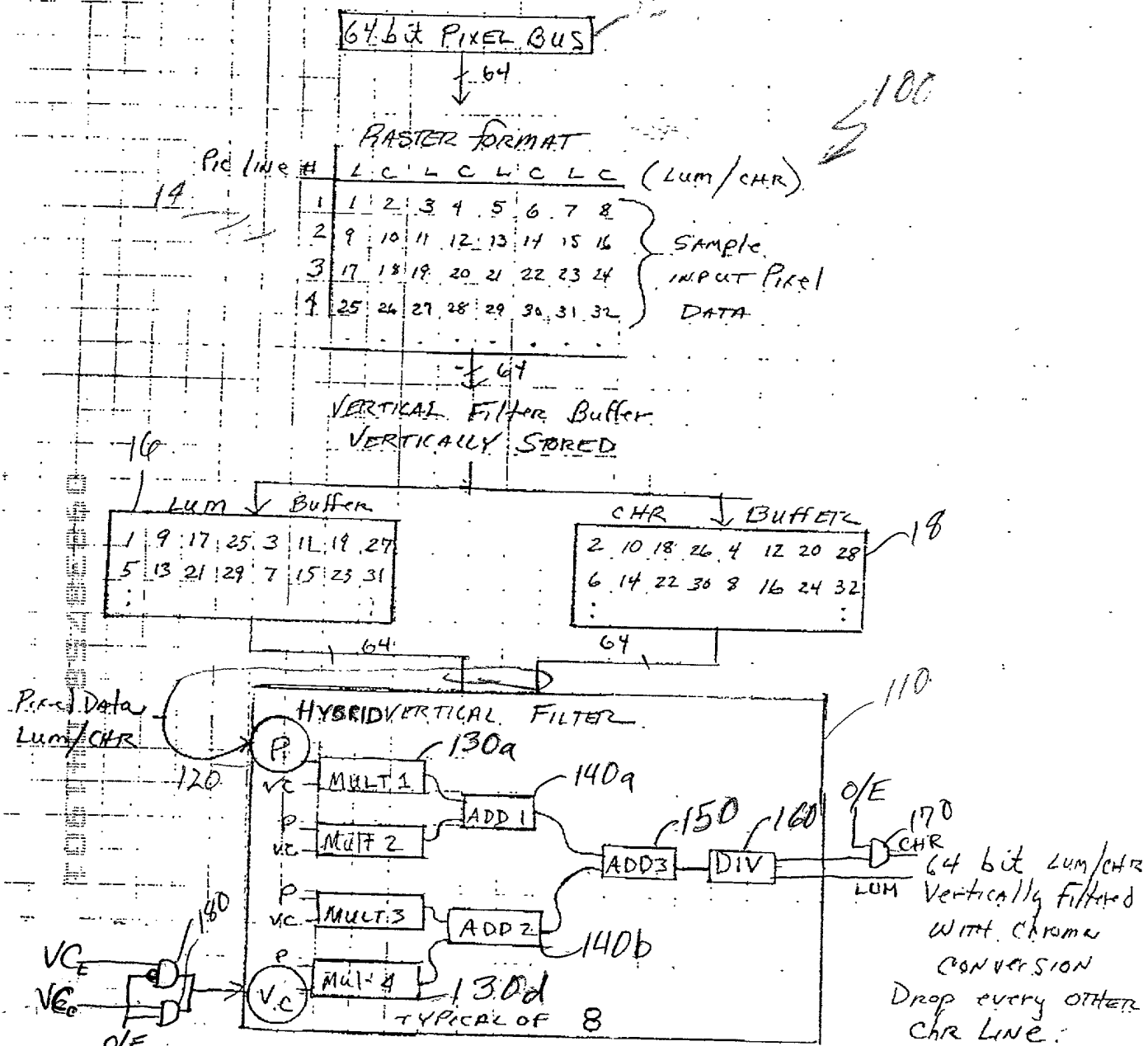


FIGURE 3 SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL FILTER/CHROMA CONVERSION CIRCUIT



$$P_{no} = P_{rc} \cdot V_c + P(V_c + V_c) + P(V_c + V_c + V_c) + P(V_c + V_c + V_c) + P(V_c + V_c) + P V_c$$